

THE Z80 INSTRUCTION SET

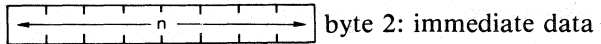
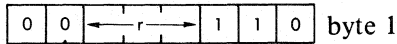
LD r, n

Load register r with immediate data n.

Function:

$$r \leftarrow n$$

Format:

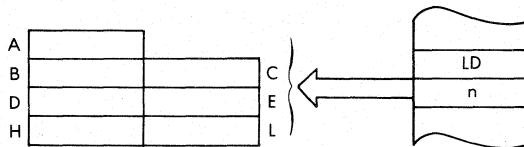


Description:

The contents of the memory location immediately following the opcode location are loaded into the specified register. r may be any one of:

A - 111	E - 011
B - 000	H - 100
C - 001	L - 101
D - 010	

Data Flow:



Timing:

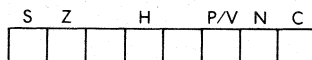
2 M cycles; 7 T states; 3.5 usec @ 2 MHz

Addressing Mode: Immediate.

Byte Codes:

r:	A	B	C	D	E	H	L
	3E	06	0E	16	1E	26	2E

Flags:



(no effect).

PROGRAMMING THE Z80

Example:

LD C, 3B

Before:

After:

