

## THE Z80 INSTRUCTION SET

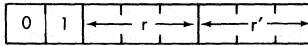
### **LD r, r'**

Load register r from register r'.

*Function:*

$$r \leftarrow r'$$

*Format:*

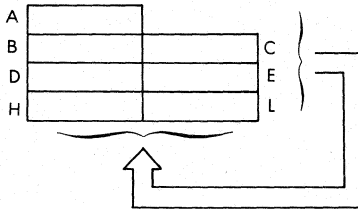


*Description:*

The contents of the specified source register are loaded into the specified destination register. r and r' may be any one of:

A - 111	E - 011
B - 000	H - 100
C - 001	L - 101
D - 010	

*Data Flow:*



*Timing:*

1 M cycle; 4 T states; 2 usec @ 2 MHz

*Addressing Mode:*

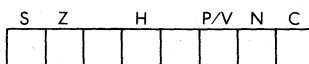
Implicit.

*Byte Codes:*

	A	B	C	D	E	H	L	(source)
A	7F	78	79	7A	7B	7C	7D	
B	47	40	41	42	43	44	45	
C	4F	48	49	4A	4B	4C	4D	
D	57	50	51	52	53	54	55	
E	5F	58	59	5A	5B	5C	5D	
H	67	60	61	62	63	64	65	
L	6F	68	69	6A	6B	6C	6D	

(dest.)

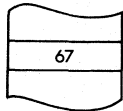
*Flags:*



(no effect).

# PROGRAMMING THE Z80

*Example:* LD H, A



OBJECT CODE

Before:

A 8C

H 8D

After:

A 8C

H 8C