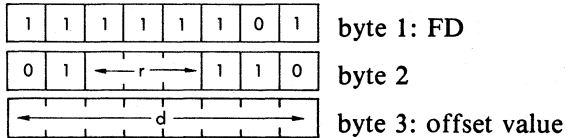


THE Z80 INSTRUCTION SET

LD r, (IY + d) Load register r indirect from indexed memory location (IY + d)

Function: $r \leftarrow (IY + d)$

Format:

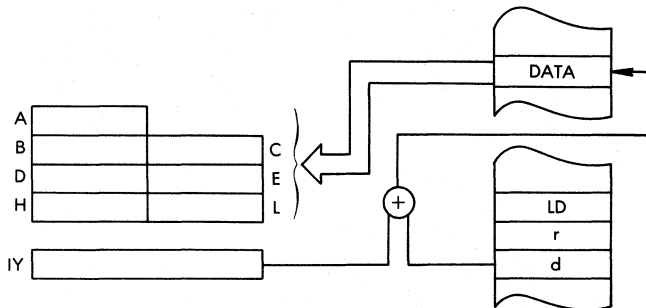


Description:

The contents of the memory location addressed by the IY index register plus the given offset value, are loaded into the specified register. r may be any one of:

A - 111	E - 011
B - 000	H - 100
C - 001	L - 101
D - 010	

Data Flow:

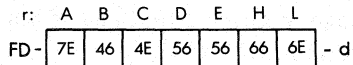


Timing: 5 M cycles, 19 T states; 9.5 usec @ 2 MHz

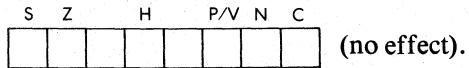
Addressing Mode: Indexed.

PROGRAMMING THE Z80

Byte Codes:



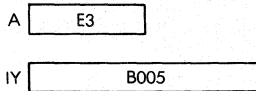
Flags:



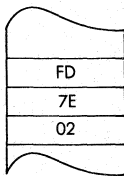
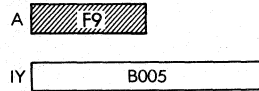
Example:

LD A, (IY + 2)

Before:



After:



OBJECT CODE

