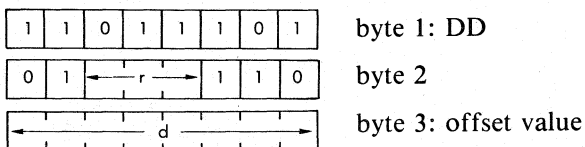


THE Z80 INSTRUCTION SET

LD r, (IX + d) Load register r indirect from indexed memory location (IX + d)

Function: $r \leftarrow (IX + d)$

Format:

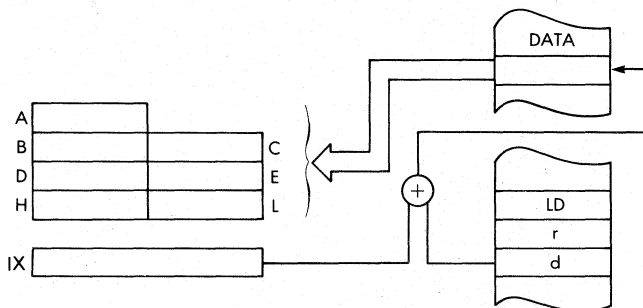


Description:

The contents of the memory location addressed by the IX index register plus the given offset value, are loaded into the specified register. r may be any one of:

- | | |
|---------|---------|
| A - 111 | E - 011 |
| B - 000 | H - 100 |
| C - 001 | L - 101 |
| D - 010 | |

Data Flow:



Timing: 5 M cycles; 19 T states; 9.5 usec @ 2 MHz

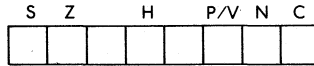
Addressing Mode: Indexed.

Byte Codes:

r:	A	B	C	D	E	H	L	
DD-	7E	46	4E	56	5E	66	6E	-d

PROGRAMMING THE Z80

Flags:



(no effect).

Example:

LD E, (IX + 5)

Before:

After:

