

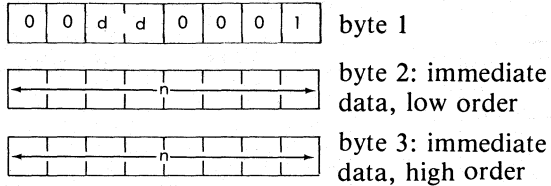
**LD dd, nn**

Load register pair dd with immediate data nn.

*Function:*

dd ← nn

*Format:*

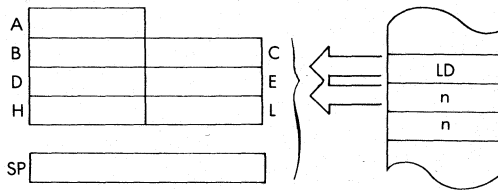


*Description:*

The contents of the two memory locations immediately following the opcode are loaded into the specified register pair. The lower order byte of the data occurs immediately after the opcode. dd may be any one of:

- BC — 00
- DE — 01
- HL — 10
- SP — 11

*Data Flow:*



*Timing:*

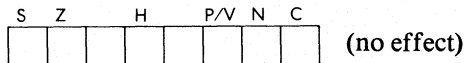
3 M cycles; 10 T states; 5 usec @ 2 MHz

*Addressing Mode:* Immediate.

*Byte Codes:*

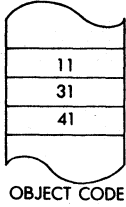
dd:	BC	DE	HL	SP
	01	11	21	31

*Flags:*

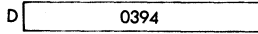


# PROGRAMMING THE Z80

*Example:* LD DE, 4131



Before:



After:

