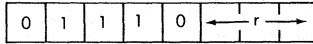


THE Z80 INSTRUCTION SET

LD (HL), r Load indirectly addressed memory location (HL) from register r.

Function: (HL) ← r

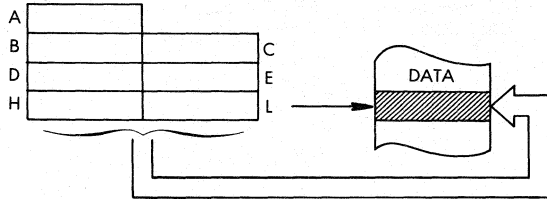
Format:



Description: The contents of the specified register are loaded into the memory location addressed by the HL register pair. r may be any one of:

| | |
|---------|---------|
| A - 111 | E - 011 |
| B - 000 | H - 100 |
| C - 001 | L - 101 |
| D - 010 | |

Data Flow:



Timing: 2 M cycles; 7 T states; 3.5 usec @ 2 MHz

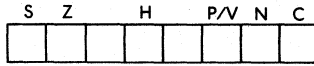
Addressing Mode: Indirect.

Byte Codes:

| | | | | | | | |
|----|----|----|----|----|----|----|----|
| r: | A | B | C | D | E | H | L |
| | 77 | 70 | 71 | 72 | 73 | 74 | 75 |

PROGRAMMING THE Z80

Flags:

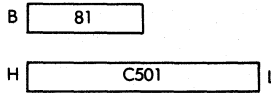


(no effect).

Example:

LD (HL), B

Before:



After:

