LD (BC), A
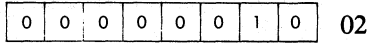
Load indirectly addressed memory location (BC) from the accumulator.

Function: (BC) ← A

Format:

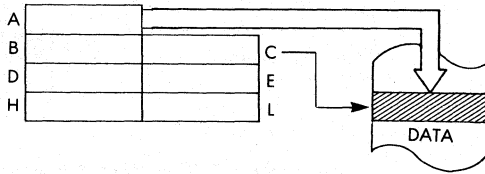| 0	0	0	0	0	0	1	0

02

Description: The contents of the accumulator are loaded into the memory location addressed by the contents of the BC register pair.
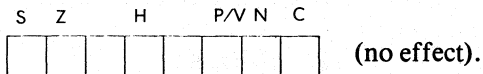
Data Flow:

Timing: 2 M cycles; 7 T states; 3.5 usec @ 2 MHz

Addressing Mode: Indirect.
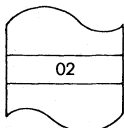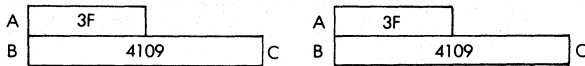
Flags:

S Z H P/V N C

(no effect).

Example: LD (BC), A

Before: After:

OBJECT CODE