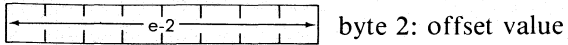
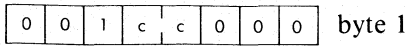


PROGRAMMING THE Z80

JR cc, e Jump e relative on condition.

Function: if cc true, $PC \leftarrow PC + e$

Format:

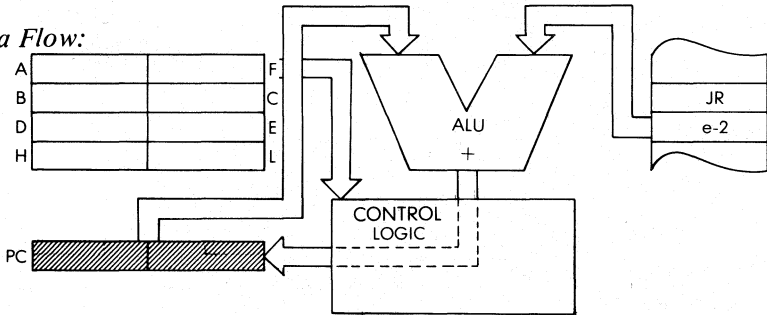


Description:

If the specified condition is met, the given offset value is added to the program counter using two's complement arithmetic so as to enable both forward and backward jumps. The offset value is added to the value of PC + 2 (after the jump). As a result, the effective offset is -126 to +129 bytes. The assembler automatically subtracts the value of PC + 2 from the source offset value to generate the hex code. If the condition is not met, the offset value is ignored and instruction execution continues in sequence. cc may any one of:

- NZ - 00
- Z - 01
- NC - 10
- C - 11

Data Flow:



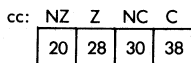
Timing:

	<i>M cycles:</i>	<i>T states:</i>	<i>usec</i> <i>@ 2 MHz:</i>
condition met:	3	12	6
condition not met:	2	7	3.5

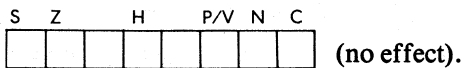
THE Z80 INSTRUCTION SET

Addressing Mode: Relative.

Byte Codes:



Flags:



Example:

JR NC, \$ - 3 \$ = current PC

Before:

After:

