

PROGRAMMING THE Z80

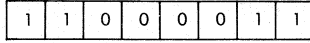
JP pq

Jump to location pq.

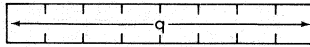
Function:

PC ← pq

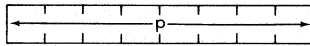
Format:



byte 1: C3



byte 2: address, low order

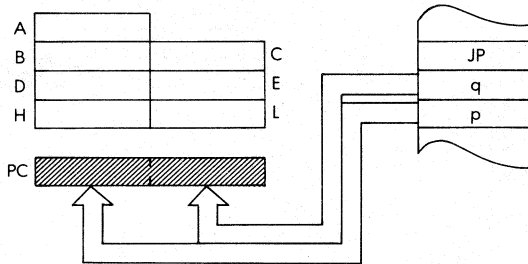


byte 3: address, high order

Description:

The contents of the memory location immediately following the opcode are loaded into the low order half of the program counter and the contents of the second memory location immediately following the opcode are loaded into the high order of the program counter. The next instruction will be fetched from this new address.

Data Flow:



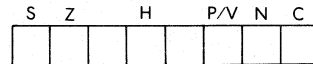
Timing:

3 M cycles; 10 T states; 5 usec @ 2 MHz

Addressing Mode:

Immediate.

Flags:



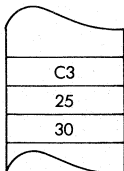
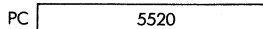
(No effect)

Example:

JP 3025

Before:

After:



OBJECT CODE