JP (IY)

Jump to IY.

Function: PC ← IY

Format:

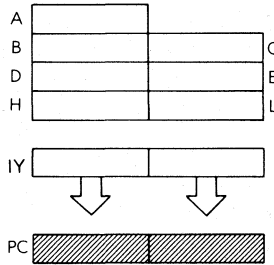| 1 | 1 | 1 | 1 | 1 | 1 | 0 | 1 |

byte 1: FD

| 1 | 1 | 1 | 0 | 1 | 0 | 0 | 1 |
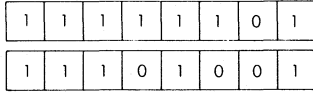
byte 2: E9

Description: The contents of the IY register are moved into the program counter. The next instruction will be fetched from this new address.
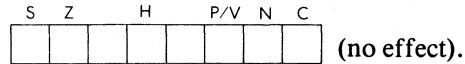
Data Flow:

Timing: 2 M cycles; 8 T states; 4 usec @ 2 MHz
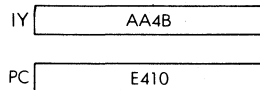
Addressing Mode: Implicit.

Flags:

S Z   H   P/V N C

(no effect).

Example: JP   (IY)

Before: After:

| FD |
| E9 |

OBJECT CODE

IY [AA4B]   IY [AA4B]

PC [E410]   PC [AA4B]

227