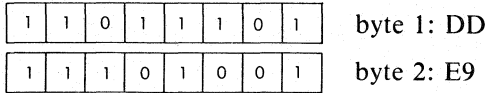


PROGRAMMING THE Z80

JP (IX) Jump to IX.

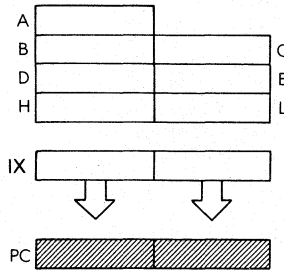
Function: PC ← IX

Format:



Description: The contents of the IX register are loaded into the program counter. The next instruction is fetched from this new address.

Data Flow:



Timing: 2 M cycles; 8 T states; 4 usec @ 2 MHz

Addressing Mode: Implicit.

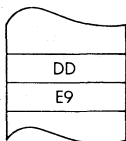
Flags: S Z H P/V N C

(no effect).

Example: JP (IX)

Before:

After:



OBJECT CODE

