INIR

Block input with increment.

Function: (HL) ← (C); B ← B − 1; HL ← HL + 1; Repeat until B = 0
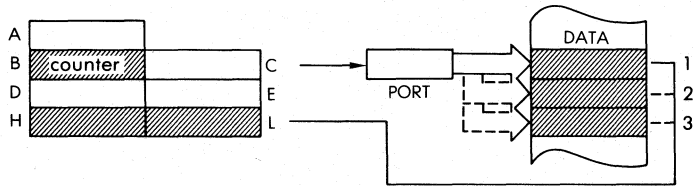
Format:

| 1 | 1 | 1 | 0 | 1 | 1 | 0 | 1 | byte 1: ED

| 1 | 0 | 1 | 1 | 0 | 0 | 1 | 0 | byte 2: B2

Description: The peripheral device addressed by the C register is read and the result is loaded into the memory location addressed by the HL register pair. The B register is decremented and the HL register pair is incremented. If B is not zero, the program counter is decremented by 2 and the instruction is re-executed.

Data Flow:

Timing: B = 0: 4 M cycles; 16 T states; 8 used @ 2 MHz.
B ≠ 0: 5 M cycles; 21 T states; 10.5 usec @ 2 MHz.
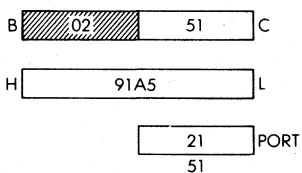
Addressing Mode: External.

Flags:

S	Z		H		P/V	N	C
?	1		?		?	1	

Example: INIR

Before: After:

```
B  ///02///   51   C      B  ///00///   51   C
H    91A5      L           H   ///91A7///    L
           21  PORT                 ///B5///  PORT
           51                         51
```

```
        ED              91A5   8F        91A5  ///21///
        B2              91A6   3D        91A6  ///B5///
                        91A7   09        91A7   09
    OBJECT CODE
```