INI

Input with increment.

Function: (HL) ← (C); B ← B − 1; HL ← HL + 1

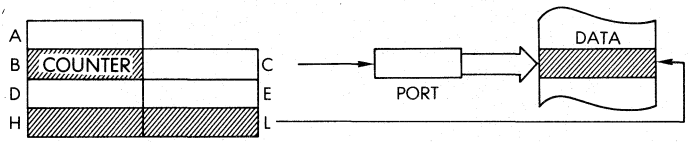
Format:

1	1	1	0	1	1	0	1	byte 1: ED
1	0	1	0	0	0	1	0	byte 2: A2

Description: The peripheral device addressed by the C register is read and the result is loaded into the memory location addressed by the HL register pair. The B register is decremented and the HL register pair is incremented.

The contents of C are placed on the low half of the address bus. The contents of B are placed on the high half. I/O selection is generally made by C, i.e., by A0 to A7. B is a byte counter.

Data Flow:



Timing: 4 M cycles; 16 T states; 8 usec @ 2 MHz

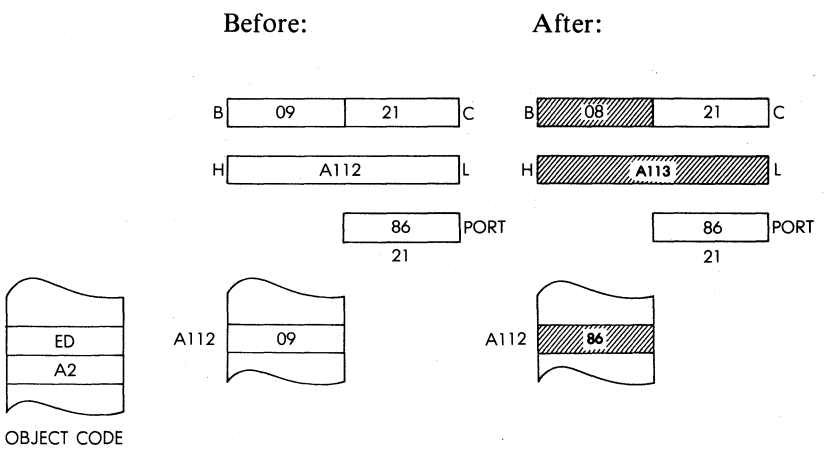
Addressing Mode: External.

Flags:

S	Z		H		P/V	N	C
?	X		?		?	1	

Z is set if B = 0 after execution,
Reset otherwise

Example: INI


Before:
After:
B
09
21
C
H
A112
L
86
PORT
21
ED
A2
OBJECT CODE
A112
09
B
08
21
C
H
A113
L
86
PORT
21
A112
86