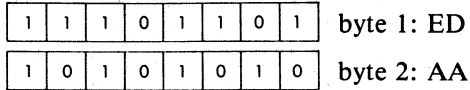


PROGRAMMING THE Z80

IND Input with decrement.

Function: $(HL) \leftarrow (C)$; $B \leftarrow B - 1$; $HL \leftarrow HL - 1$

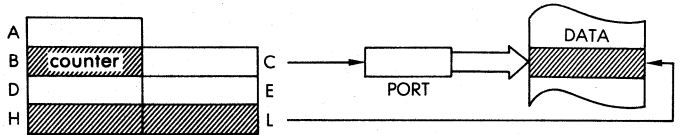
Format:



Description:

The peripheral device addressed by the C register is read and the result is loaded into the memory location addressed by the HL register pair. The B register and the HL register pair are then each decremented.

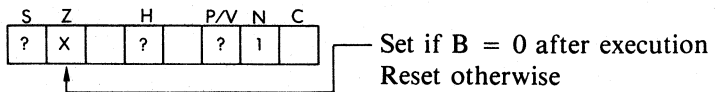
Data Flow:



Timing: 4 M cycles; 16 T states; 8 usec @ 2 MHz

Addressing Mode: External.

Flags:



THE Z80 INSTRUCTION SET

Example:

IND

Before:

After:

