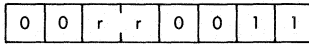


## THE Z80 INSTRUCTION SET

**INC rr**                      Increment register pair rr.

*Function:*                       $rr \leftarrow rr + 1$

*Format:*

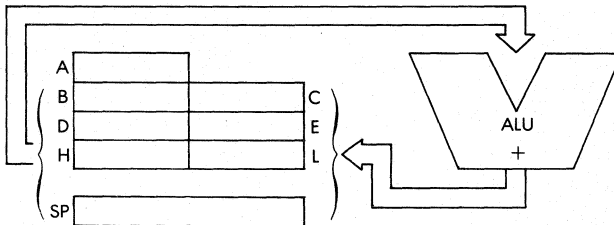


*Description:*

The contents of the specified register pair are incremented and the result is stored back in the register pair. rr may be any one of:

BC - 00	HL - 10
DE - 01	SP - 11

*Data Flow:*



*Timing:*                              1 M cycle; 6 T states; 3 usec @ 2 MHz

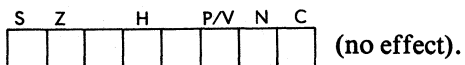
*Addressing Mode:*      Implicit.

*Byte Codes:*

rr:	BC	DE	HL	SP
	03	13	23	33

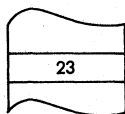
# PROGRAMMING THE Z80

*Flags:*



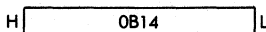
*Example:*

INC HL



OBJECT  
CODE

Before:



After:

