

PROGRAMMING THE Z80

INC r *Increment register r.*

Function: $r \leftarrow r + 1$

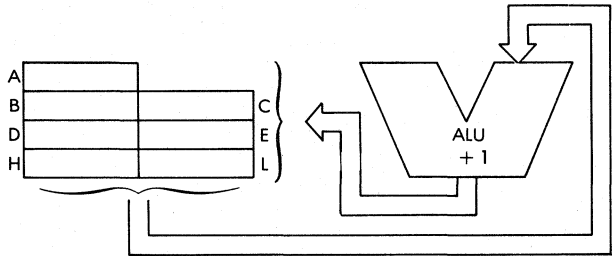
Format:

0	0	← r →	1	0	0
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Description: The contents of the specified register are incremented. r may be any one of:

- | | |
|---------|---------|
| A - 111 | E - 011 |
| B - 000 | H - 100 |
| C - 001 | L - 101 |
| D - 010 | |

Data Flow:



Timing: 1 M cycle; 4 T states; 2 usec @ 2 MHz

Addressing Mode: Implicit.

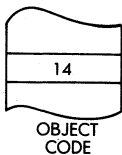
Byte Codes: $r:$ A B C D E H L

3C	04	0C	14	1C	24	2C
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Flags:

S	Z		H		P	N	C
●	●		●		●	○	

Example: INC D



Before:

D	06
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After:

D	07
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