PROGRAMMING THE Z80

IM 2 Set interrupt mode 2 condition.

Function:

Internal interrupt control.

Format:



Description:

Set interrupt mode 2. When an interrupt occurs, one byte of data must be provided by the peripheral which is used as the low order of an address. The high order of this vector address is taken from the contents of the I register. This points to a second address stored in memory, which is loaded into the program counter and begins execution.

Timing:

2 M cycles; 8 T states; 4 usec @ 2 MHz

Addressing Mode: Implicit.

Flags:

S Z H P/V N C (no effect)