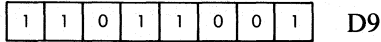


PROGRAMMING THE Z80

EXX Exchange alternate registers.

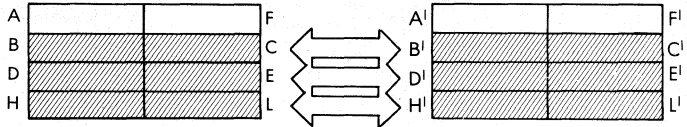
Function: BC ↔ BC'; DE ↔ DE'; HL ↔ HL'

Format:



Description: The contents of the general purpose registers are exchanged with the contents of the corresponding alternate registers.

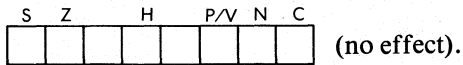
Data Flow:



Timing: 1 M cycle; 4 T states; 2 usec @ 2 MHz

Addressing Mode: Implicit.

Flags:



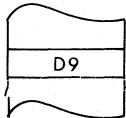
Example: EXX

Before:

A	04	2B	F
B	39	26	C
D	54	02	E
H	F1	D0	L

After:

A	04	2B	F
B	8C	00	C
D	93	D0	E
H	4F	E3	L



OBJECT CODE

A'	3F	2A	F'
B'	8C	00	C'
D'	93	D0	E'
H'	4F	E3	L'

A'	3F	2A	F'
B'	39	26	C'
D'	54	02	E'
H'	F1	D0	L'