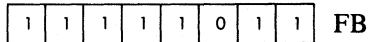


THE Z80 INSTRUCTION SET

EI Enable interrupts.

Function: IFF \leftarrow 1

Format:

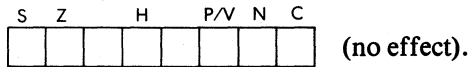


Description: The interrupt flip-flop is set, thereby enabling maskable interrupts after the execution of the instruction following the EI instruction. In the meantime maskable interrupts are disabled.

Timing: 1 M cycle; 4 T states; 2 usec @ 2 MHz

Addressing Mode: Implicit.

Flags:



Example: A usual sequence at the end of an interrupt routine is:
EI
RETI
The maskable interrupt is re-enabled following completion of RETI.