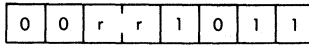


PROGRAMMING THE Z80

DEC rr Decrement register pair rr.

Function: $rr \leftarrow rr - 1$

Format:

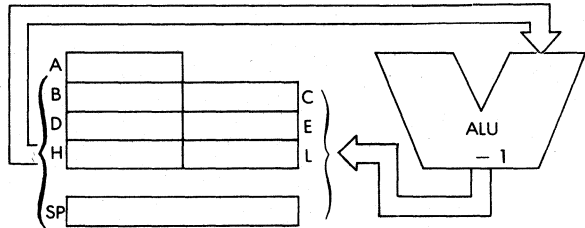


Description:

The contents of the specified register pair are decremented and the result is stored back in the register pair. rr may be any one of:

BC - 00	HL - 10
DE - 01	SP - 11

Data Flow:



Timing: 1 M cycle; 6 T states; 3 usec @ 2 MHz

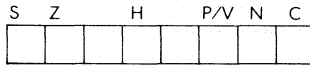
Addressing Mode: Implicit.

Byte Codes: rr:

BC	DE	HL	SP
0B	1B	2B	3B

THE Z80 INSTRUCTION SET

Flags:



(no effect).

Example:

DEC BC

Before:

After:

