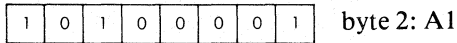
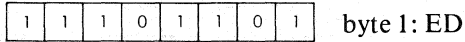


CPI

Compare with increment.

Function: $A - [HL]; HL \leftarrow HL + 1; BC \leftarrow BC - 1$

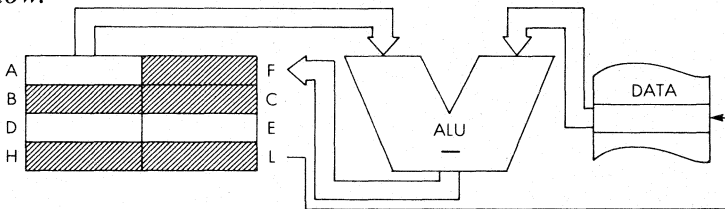
Format:



Description:

The contents of the memory location addressed by the HL register pair are subtracted from the contents of the accumulator and the result is discarded. The HL register pair is incremented and the BC register pair is decremented.

Data Flow:

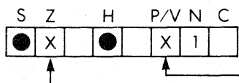


Timing:

4 M cycles; 16 T states: 8 usec @ 2 MHz

Addressing Mode: indirect.

Flags:



Reset if BC = 0 after execution set otherwise
Set if A = [HL]

PROGRAMMING THE Z80

Example:

CPI

Before:

After:

