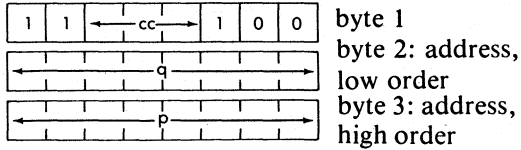


THE Z80 INSTRUCTION SET

CALL cc, pq Call subroutine on condition.

Function: if cc true: $(SP - 1) \leftarrow PC_{\text{high}}$; $(SP - 2) \leftarrow PC_{\text{low}}$; $SP \leftarrow SP - 2$; $PC \leftarrow pq$
 If cc false: $PC \leftarrow PC + 3$

Format:



Description:

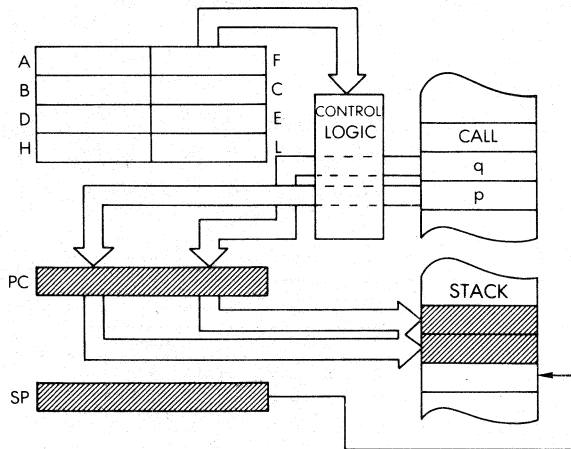
If the condition is met, the contents of the program counter are pushed onto the stack as described for the PUSH instructions. Then, the contents of the memory location immediately following the opcode are loaded into the low order of the PC and the contents of the second memory location after the the opcode are loaded into the high order half of the PC. The next instruction fetched will be from this new address. If the condition is not met, the address pq is ignored and the following instruction is executed. cc may be any one of:

- | | |
|----------|----------|
| NZ - 000 | PO - 100 |
| Z - 001 | PE - 101 |
| NC - 010 | P - 100 |
| C - 011 | M - 111 |

An RET instruction can be used at the end of the subroutine being called to restore the PC.

PROGRAMMING THE Z80

Data Flow:

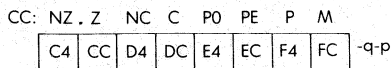


Timing:

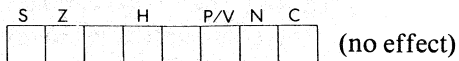
	<i>M cycles:</i>	<i>T states:</i>	<i>usec @ 2 MHz</i>
condition true:	5	17	8.5
condition not true:	3	10	5

Addressing Mode: Immediate.

Byte Codes:



Flags:

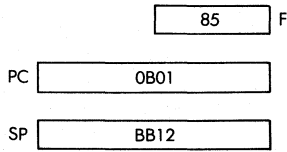


THE Z80 INSTRUCTION SET

Example:

CALL Z, B042

Before:



After:

