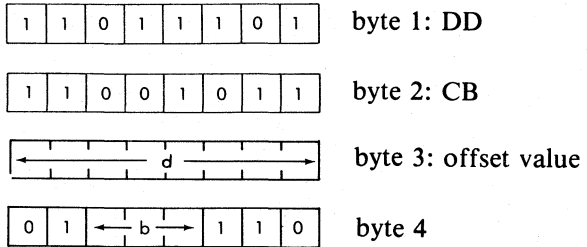


THE Z80 INSTRUCTION SET

BIT b, (IX + d) Test bit b of indexed addressed memory location (IX + d)

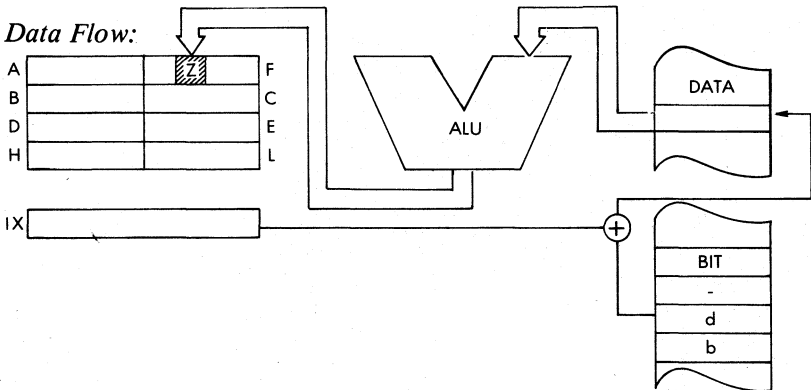
Function: $Z \leftarrow \overline{(IX + d)_b}$

Format:



Description: The specified bit of the memory location addressed by the contents of the IX register plus the given offset value is tested and the Z flag is set according to the result. b may be any one of:

- | | |
|---------|---------|
| 0 - 000 | 5 - 101 |
| 1 - 001 | 6 - 110 |
| 2 - 010 | 7 - 111 |
| 3 - 011 | |
| 4 - 100 | |



PROGRAMMING THE Z80

Timing: 5 M cycles; 20 T states: 10 usec @ 2 MHz

Addressing Mode: Indexed.

Byte Codes:

b:	0	1	2	3	4	5	6	7
DD-CB-d	46	4E	56	5E	66	6E	76	7E

Flags:

S	Z	H	P/V	N	C
?	●	1	?	0	

Example:

BIT 6, (IX + 0)

Before:

After:

