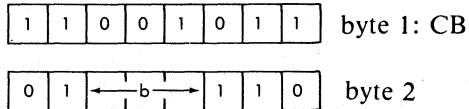


## THE Z80 INSTRUCTION SET

**BIT b, (HL)**      Test bit b of indirectly addressed memory location (HL)

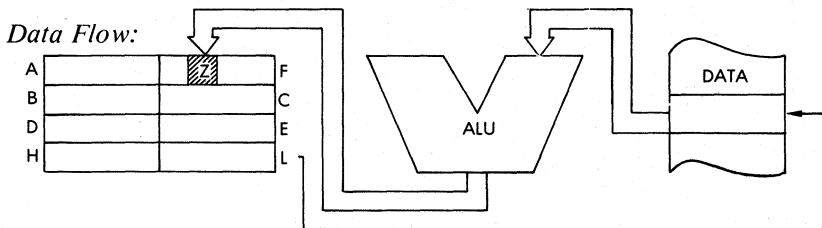
*Function:*             $Z \leftarrow \overline{(HL)}_b$

*Format:*



*Description:*            The specified bit of the memory location addressed by the contents of the HL register pair is tested and the Z flag is set according to the result. b may be any one of:

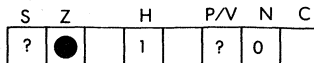
0 – 000	4 – 100
1 – 001	5 – 101
2 – 010	6 – 110
3 – 011	7 – 111



*Timing:*                3 M cycles; 12 T states; 6 usec @ 2 MHz

*Addressing Mode:* Indirect.

*Flags:*



# PROGRAMMING THE Z80

*Byte Codes:*

b:	0	1	2	3	4	5	6	7
CB-	46	4E	56	5E	66	6E	76	7E

*Example:*

BIT 3, (HL)

Before:

After:

