

## THE Z80 INSTRUCTION SET

**ADD IY, rr**      Add IY and register pair rr.

*Function:*             $IY \leftarrow IY + rr$

*Format:*

1	1	1	1	1	1	0	1
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byte 1: FD

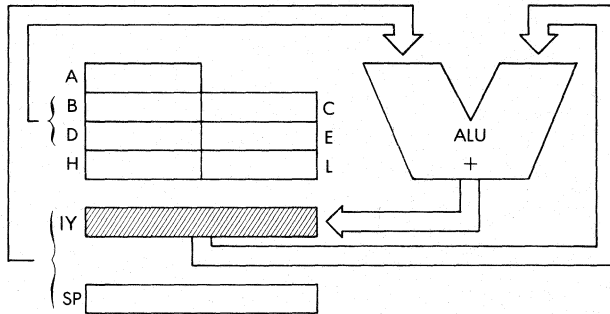
0	0	r	r	1	0	0	1
---	---	---	---	---	---	---	---

byte 2

*Description:*            The contents of the IY register are added to the contents of the specified register pair and the result is stored back in IY. rr may be any one of:

BC - 00	IY - 10
DE - 01	SP - 11

*Data Flow:*



*Timing:*                4 M cycles; 15 T states: 7.5 usec @ 2 MHz

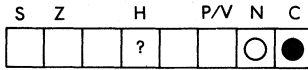
*Addressing Mode:*    Implicit.

*Byte Codes:*

rr:	BC	DE	IY	SP
FD-	09	19	29	39

# PROGRAMMING THE Z80

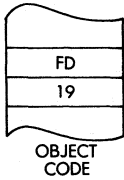
*Flags:*



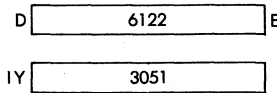
H is set by carry out of bit 11.  
C is set by carry out of bit 15.

*Example:*

**ADD IY, DE**



**Before:**



**After:**

