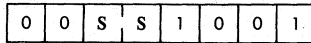


THE Z80 INSTRUCTION SET

ADD HL, ss Add HL and register pair ss.

Function: $HL \leftarrow HL + ss$

Format:

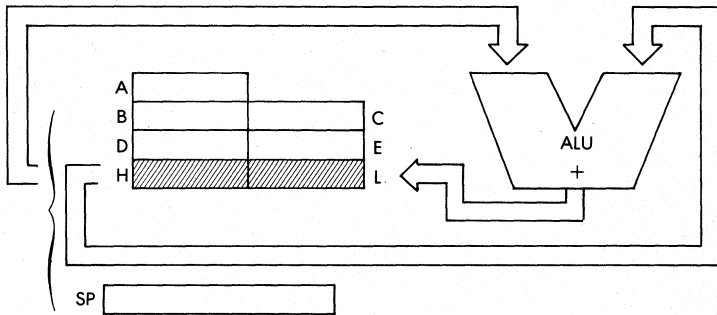


Description:

The contents of the specified register pair are added to the contents of the HL register pair and the result is stored in HL. ss may be any one of:

BC - 00	HL - 10
DE - 01	SP - 11

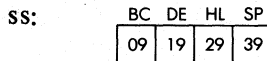
Data Flow:



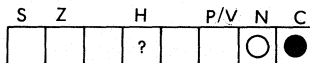
Timing: 3 M cycles; 11 T states: 5.5 usec @ 2 MHz

Addressing Mode: Implicit.

Byte Codes:



Flags:



C is set by carry from bit 15, reset otherwise.

H is set by a carry from bit 11

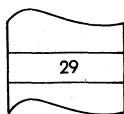
PROGRAMMING THE Z80

Example:

ADD HL, HL

Before:

After:



OBJECT
CODE

