

THE Z80 INSTRUCTION SET

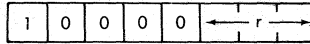
ADD A, r

Add accumulator with register r.

Function:

$$A \leftarrow A + r$$

Format:

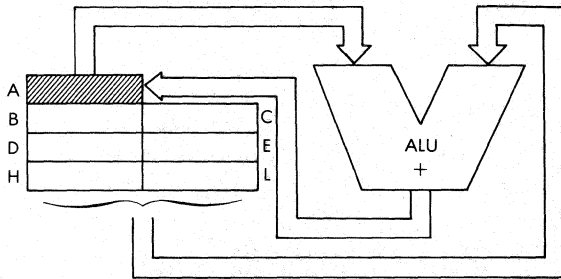


Description:

The contents of the accumulator are added with the contents of the specified register. The result is placed in the accumulator. r may be any one of:

A - 111	E - 011
B - 000	H - 100
C - 001	L - 101
D - 010	

Data Flow:



Timing:

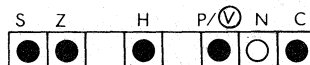
1 M cycle; 4 T states: 2 usec @ 2 MHz.

Addressing Mode: Implicit.

Byte Codes:

r:	A	B	C	D	E	H	L
	87	80	81	82	83	84	85

Flags:

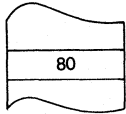


PROGRAMMING THE Z80

Example: **ADD A, B**

Before:

After:



OBJECT CODE

A

3D

B

02

A

3F

B

02
