ADD A, (HL)

Add accumulator with indirectly addressed memory location (HL).

Function: A ← A + (HL)
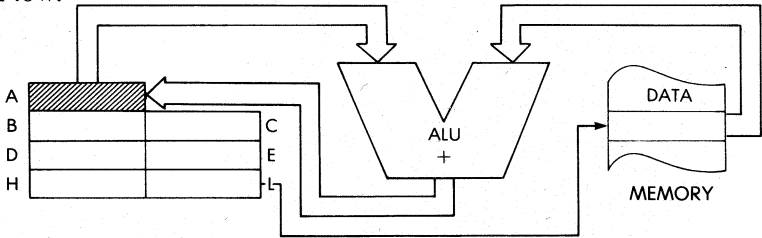
Format:

1	0	0	0	0	1	1	0

86
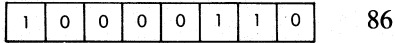
Description: The contents of the accumulator are added to the contents of the memory location addressed by the HL register pair. The result is stored in the accumulator.
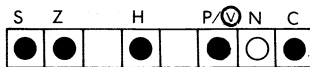
Data Flow:

Timing: 2 M cycles; 7 T states: 3.5 usec @ 2 MHz

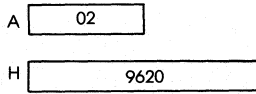*Addressing Mode:*   Indirect.

Flags:

Example: ADD   A, (HL)

Before: After:

A [02   ]   A [////// B3 //////]

H [9620   ]   H [9620   ]

86
OBJECT CODE

9620 [B1]   9620 [B1]