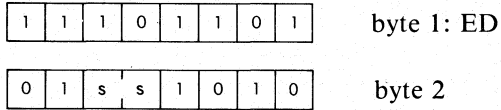


PROGRAMMING THE Z80

ADC HL, ss Add with carry HL and register pair ss.

Function: $HL \leftarrow HL + ss + C$

Format:

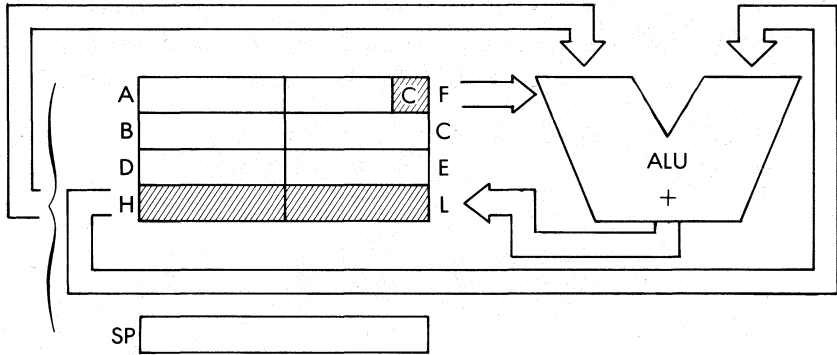


Description:

The contents of the HL register pair are added to the contents of the specified register pair, and then the contents of the carry flag are added. The final result is stored back in HL. ss may be any one of:

- | | |
|---------|---------|
| BC - 00 | HL - 10 |
| DE - 01 | SP - 11 |

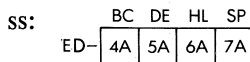
Data Flow:



Timing: 4 M cycles; 15 T states; 75 usec @ 2 MHz

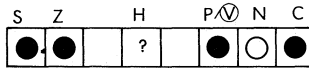
Addressing Mode: Implicit.

Byte Codes:



THE Z80 INSTRUCTION SET

Flags:



H is set if there is a carry from bit 11.

Example:

ADC HL, DE

Before:

After:

