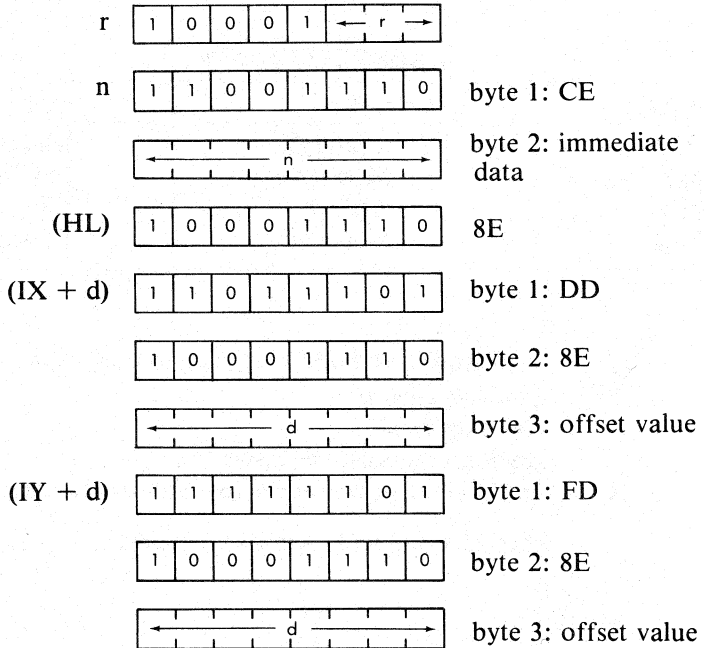


PROGRAMMING THE Z80

ADC A, s Add accumulator and specified operand with carry.

Function: $A \leftarrow A + s + C$

Format: s: may be r, n, (HL), (IX + d), or (IY + d)



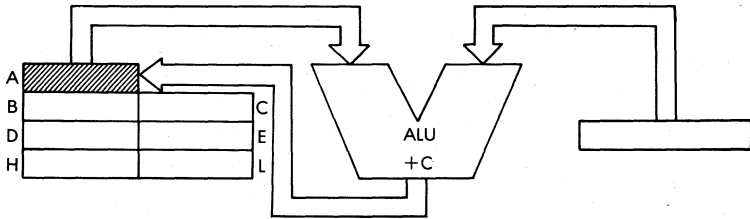
r may be any one of:

A - 111	E - 011
B - 000	H - 100
C - 001	L - 101
D - 010	

THE Z80 INSTRUCTION SET

Description: The operand *s* and the carry flag *C* from the status register are added to the accumulator, and the result is stored in the accumulator. *s* is defined in the description of the similar **ADD** instructions.

Data Flow:



Timing:

<i>s</i> :	<i>M</i> cycles:	<i>T</i> states:	<i>usec</i> @ 2 MHz:
r	1	4	2
n	2	7	3.5
(HL)	2	7	3.5
(IX + d)	5	19	9.5
(IY + d)	5	19	9.5

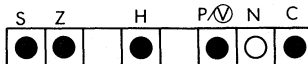
Addressing Mode: r: implicit; n: immediate; (HL): indirect; (IX + d), (IY + d): indexed.

Byte Codes:

ADC A,r r:

A	B	C	D	E	H	L
8F	88	89	8A	8B	8C	8D

Flags:

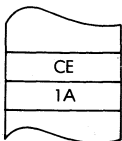


Example:

ADC A, 1A

Before:

After:



OBJECT CODE

